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## APPLICANTS

Jae-Soon Lim, Seoul-city, KOREA, REPUBLIC OF;

Sung-Tae Kim, Gyeonggi-do, KOREA, REPUBLIC OF;  
 Young-Sun Kim, Gyeonggi-do, KOREA, REPUBLIC OF; Ki-Hyun Hwang, Gyeonggi-do, KOREA, REPUBLIC OF;  
 Gab-Jin Nam, Gyeonggi-do, KOREA, REPUBLIC OF;  
 Ki-Chul Kim, Gyeonggi-do, KOREA, REPUBLIC OF;  
 Joo-Won Lee, Gyeonggi-do, KOREA, REPUBLIC OF;  
 Jae-Young Park, Gyeonggi-do, KOREA, REPUBLIC OF;

\*\* CONTINUING DATA \*\*\*\*\* *JMJ**None*\*\* FOREIGN APPLICATIONS \*\*\*\*\* *JMJ*

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## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 10/27/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY KOREA, REPUBLIC OF	SHEETS DRAWING 10	TOTAL CLAIMS 30	INDEPENDENT CLAIMS 3
Verified and Acknowledged Examiner's Signature <i>Joseph M. Thomas</i> <i>JMJ</i> Initials					

## ADDRESS

20792  
 MYERS BIGEL SIBLEY & SAJOVEC  
 PO BOX 37428  
 RALEIGH , NC  
 27627

## TITLE

Methods for forming a capacitor on an integrated circuit device at reduced temperatures

<input type="checkbox"/> All Fees
<input type="checkbox"/> 1.16 Fees ( Filing )
<input type="checkbox"/> 1.17 Fees ( Processing Ext. of Time )

FILING FEE FEES: Authority has been given in Paper  
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